

Freeform Search

Database:	US Pre-Grant Publication Full-Text Database
	US Patents Full-Text Database
	US OCR Full-Text Database
	EPO Abstracts Database
	JPO Abstracts Database
	Derwent World Patents Index
	IBM Technical Disclosure Bulletins

Term:	<input type="text" value="l1 and L14"/>
--------------	---

Display:	<input type="text" value="20"/>	Documents in Display Format:	<input type="text" value="-"/>	Starting with Number	<input type="text" value="1"/>
-----------------	---------------------------------	-------------------------------------	--------------------------------	-----------------------------	--------------------------------

Generate: ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search

Clear

Interrupt

Search History

DATE: Wednesday, January 03, 2007 [Purge Queries](#) [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count**Set Name**

result set

DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L15</u>	l1 and L14	11	<u>L15</u>
<u>L14</u>	l12 and l13	2023	<u>L14</u>
<u>L13</u>	lru or (least adj recently adj used)	8884	<u>L13</u>
<u>L12</u>	711/(133-136).ccls.	89426	<u>L12</u>
<u>L11</u>	cach\$ with l1	16	<u>L11</u>
<u>L10</u>	l6 and L9	41	<u>L10</u>
<u>L9</u>	l2 and l1	244	<u>L9</u>
<u>L8</u>	l6 and L7	5	<u>L8</u>
<u>L7</u>	l3 near3 l1	66	<u>L7</u>
<u>L6</u>	711/(118-146).ccls.	89426	<u>L6</u>
<u>L5</u>	l1 with l2 with l3	4	<u>L5</u>
<u>L4</u>	l2 near3 l1 near2 l3	2	<u>L4</u>
<u>L3</u>	state or status	5345298	<u>L3</u>
<u>L2</u>	cache	115829	<u>L2</u>
<u>L1</u>	(eras\$ or delet\$) near2 (pending or scheduled)	856	<u>L1</u>



Welcome United States Patent and Trademark Office

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "((((cache <and> replacement)<in>metadata))<and>(lru <or> (least recently u..."

☒ e-mail

Your search matched 7 of 230 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

[View Session History](#)
[New Search](#)

Modify Search

☐ Check to search only within this results set
Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

 [Select All](#) [Deselect All](#)

- ☐ 1. **SF-LRU cache replacement algorithm**
Alghazo, J.; Akaaboune, A.; Botros, N.;
Memory Technology, Design and Testing, 2004. Records of the 2004 International Conference on
9-10 Aug. 2004 Page(s):19 - 24
Digital Object Identifier 10.1109/MTDT.2004.1327979
[AbstractPlus](#) | Full Text: [PDF\(264 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 2. **Two Cache Replacement Algorithms Based on Association Rules and Machine Learning**
Semantics, Knowledge and Grid, 2005. SKG '05. First International Conference on
2005 Page(s):28 - 28
Digital Object Identifier 10.1109/SKG.2005.136
[AbstractPlus](#) | Full Text: [PDF\(111 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 3. **Emulating Web cache replacement algorithms versus a real system**
Cardenas, L.G.; Gil, J.A.; Sahuquillo, J.; Pont, A.;
Computers and Communications, 2005. ISCC 2005. Proceedings. 10th IEEE Symposium on
27-30 June 2005 Page(s):891 - 897
Digital Object Identifier 10.1109/ISCC.2005.63
[AbstractPlus](#) | Full Text: [PDF\(464 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 4. **A space-efficient caching mechanism for flash-memory address translation**
Chin-Hsien Wu; Tei-Wei Kuo; Chia-Lin Yang;
Object and Component-Oriented Real-Time Distributed Computing, 2006. ISO
IEEE International Symposium on
24-26 April 2006 Page(s):8 pp.
Digital Object Identifier 10.1109/ISORC.2006.13
[AbstractPlus](#) | Full Text: [PDF\(256 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 5. **Impact of Technology Scaling on Energy Aware Execution Cache-Based Microarchitectures**
Talpes, E.; Marculescu, D.;
Low Power Electronics and Design, 2004. ISLPED '04. Proceedings of the 2004 Symposium on
2004 Page(s):50 - 53

[AbstractPlus](#) | [Full Text: PDF\(160 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)



6. Lookahead scheduling requests for multisize page caching

Kiniwa, J.; Hamada, T.; Mizoguchi, D.;

[Computers, IEEE Transactions on](#)

Volume 50, Issue 9, Sept. 2001 Page(s):972 - 983

Digital Object Identifier 10.1109/12.954511

[AbstractPlus](#) | [References](#) | [Full Text: PDF\(488 KB\)](#) [IEEE JNL](#)
[Rights and Permissions](#)



7. A data replica replacement algorithm based on value model in mobile gri

Xiaodong Yan; Huimin Xu; Yajing Xu; Lianxi Liu;

[Mobile Technology, Applications and Systems, 2005 2nd International Confere](#)

15-17 Nov. 2005 Page(s):4 pp.

[AbstractPlus](#) | [Full Text: PDF\(296 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)

Indexed by
 Inspec®

[Help](#) [Contact Us](#) [Privacy &](#)

© Copyright 2006 IEEE –

IEEE Xplore
RELEASE 2.1[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) |

Welcome United States Patent and Trademark Office

☐ [Search Session History](#)[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Edit an existing query or
compose a new query in the
Search Query Display.

Wed, 3 Jan 2007, 1:09:56 PM EST

Search Query Display

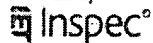
Select a search number (#)
to:

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

Recent Search Queries

- #1 ((cache <and> replacement)<in>metadata)
- #2 (((cache <and> replacement)<in>metadata)<AND>(lru <or> (least recently used)<in>metadata))
- #3 ((((((cache <and> replacement)<in>metadata)<and>(lru <or> (least recently used)<in>metadata))))<AND>(eras* or delet*<in>metadata))
- #4 ((((((cache <and> replacement)<in>metadata)<and>(lru <or> (least recently used)<in>metadata))))<AND>(eras* or delet*<in>metadata))

Indexed by

[Help](#) [Contact Us](#) [Privacy &](#)

© Copyright 2006 IEEE —